DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Electronic Version v11
Stylesheet Version v10

Title of Invention

SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR CIRCUIT

As the below named inventor, I declare that:

This declaration is directed to the invention titled: "SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR CIRCUIT"

I believe that I am the original and first inventor of the subject matter which is claimed and for which a patent is sought;

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of my knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR:

Inventor: Chi-Ming Hsiao	Inventor
Signature :	Citizen of : TW

DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

ate:		Chi - Miny Haino 09/03/2003
	Printed Name:	Chi-Ming Hsiao
	Post Office Address:	10F-3, No. 141, Kung-Hsueh N. Rd., Tai-Chung City,
	and Residence	Taiwan, R.O.C.
	Citizen of:	R.O.C.

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COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

I believe I am the sole (if only one name appears below), or joint (if more than one name appears), original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

"SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR CIRCUIT"

•	the above entitled invention is filed herewith.	
The specification for the above entitled invention was filed previously		
th application serial number:	Filing Date:	
	have reviewed and understand the contents of the above identified laims, as amended by any amendment referred to above.	
the invention disclosed in the Regulations, Section 1.56 (a application to disclose to the F to the patentability of the inv	to disclose information which is material to the patentability of his application in accordance with Title 37, Code of Federal D. I further acknowledge the duty in any continuation-in-part Patent and Trademark Office all information known to be material electron disclosed in this application, as defined in 1.56, which can the filing date of the prior application and the filing date of this	
	PRIORITY CLAIM	
There is no claim of p	riority	
Continuation - in	sed on the following: part of U.S Prior Application No. 10/250,08:	
Filing Date: 200	31b[3.	
<u> P</u>	OWER OF ATTORNEY	
	or, I hereby appoint the following attorneies to prosecute this related business in the Patent and Trademark Office: Registration# 41,526 WinstonHsu@naipo.com.tw	

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